RECEIVED **CENTRAL FAX CENTER**

NOV 2 8 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	
	June 26, 2003
Inventor	Donald L. Yates
Assignee	Micron Technology, Inc.
•	021567
	enductor Processing Patterning Methods
Commissioner for Patents	
P.O. Box 1450	
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Response to August 28, 2006 Office Action

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FEE DEFICIENCY

2.

Dated:

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-209/25.

Robert C. Hyta

Reg. No. 46,791

NUMBER OF PAGES IN FACSIMILE: 20

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Appl. No. 10/609,311 Response to 8/28/2006 Office Action NOV 2 8 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No.	10/609,311
Filing Date	June 26, 2003
	Donald L. Yates
	Micron Technology, Inc.
Group Art Unit	
Examiner	
Attorney's Docket No	MI22-2236
	Semiconductor Processing Patterning Methods

RESPONSE TO AUGUST 28, 2006 OFFICE ACTION

To:

Mail Stop Amendment Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

From:

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<u>AMENDMENTS</u>